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REVISIONS

REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	23JAN23	R. WHITNEY
B	CHANGES AS PER ECR-112250	23JUN23	R. WHITNEY
C	CHANGES AS PER ECR-117094	24OCT23	R. WHITNEY

ADAA24XX-EV-B2BX

ELLORA SIGMA-DSP BASE BOARD

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

	DATE
TEMPLATE ENGINEER -	
HARDWARE SERVICES M. VALE	
HARDWARE SYSTEMS -	
TEST ENGINEER -	
COMPONENT ENGINEER N. CATALAN	
TEST PROCESS -	
HARDWARE RELEASE K. JABATAN	
DESIGNER K. MOYO	
PTD ENGINEER R. WHITNEY	
CHECKER -	

SCHEMATIC

ANALOG DEVICES

HW TYPE : Customer Evaluation Z
Product(s): ADAA245X
: N/A
PACKAGE : LeadCount-lead BodySize PackageType-family
: Pitch-pitch StyleVendor Style

<User Define>
<User Define>
<User Define>

MASTER PROJECT TEMPLATE	TESTER TEMPLATE
TBD	067706_a

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

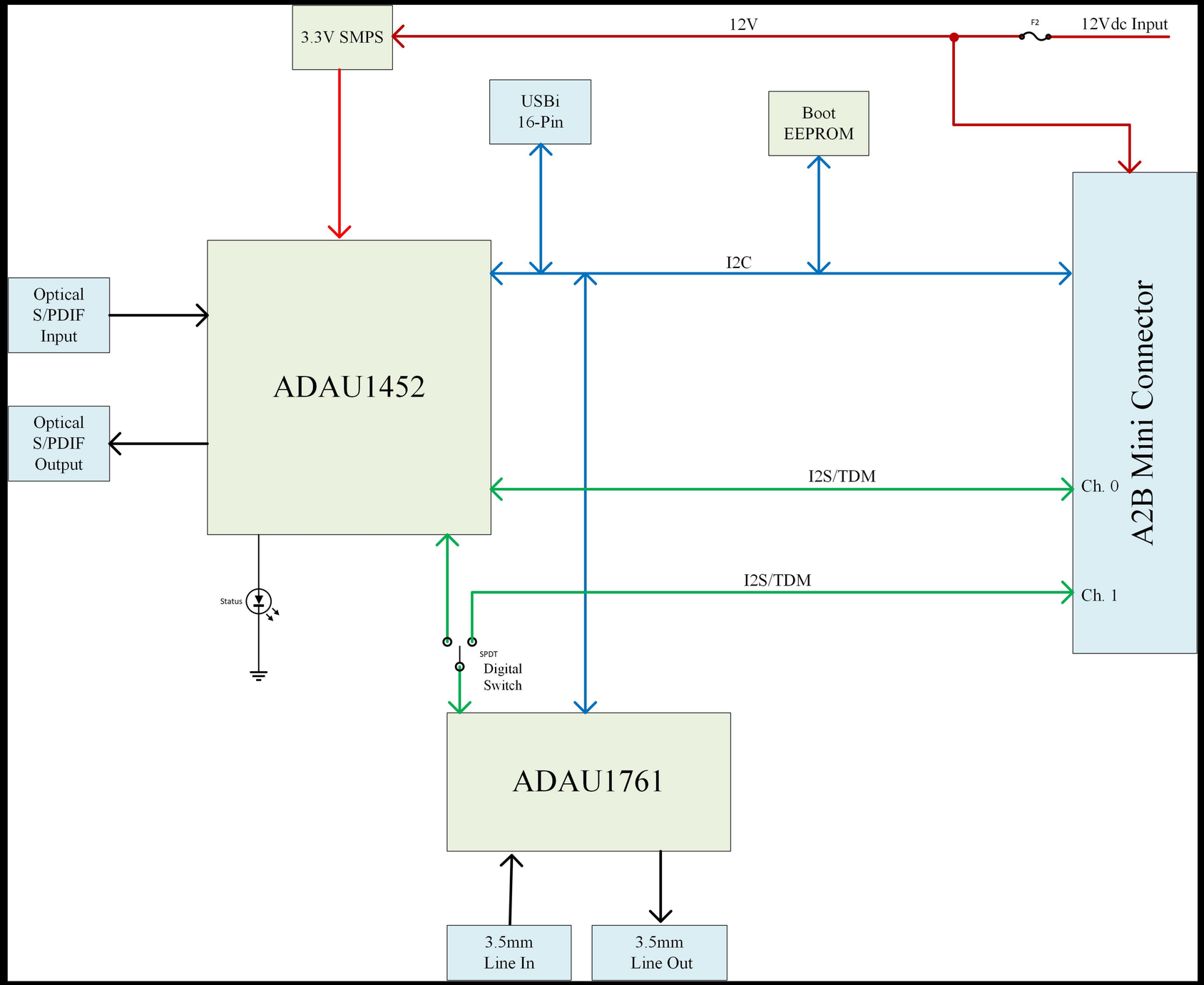
DECIMALS	FRACTIONS	ANGLES
X.XX +-0.010 X.XXX +-0.005	+-1/32	+-2

DRAWING NO. 02_073460

REV. C

SIZE	SCALE	CODE ID NO.	SHEET	OF
D	1:1	CodeID	1	6

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

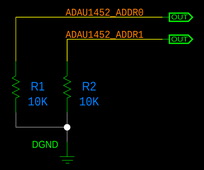


ANALOG DEVICES	SCHEMATIC	
	HW TYPE : Customer Evaluation Z Product(s): ADAA245X : N/A	
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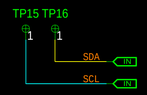
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

USBI

ADAU1452 I2C ADDRESS

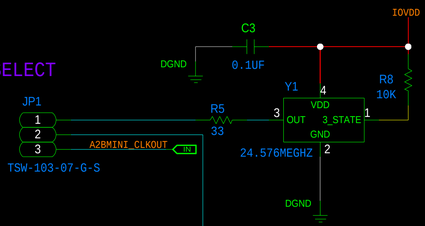


TWI ADDRESS 0111000X, 0X38
X FOR BIT 0 IS THE R/W BIT. READ - 1, WRITE - 0



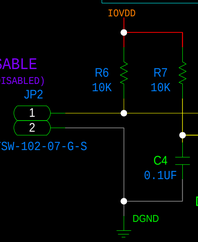
ADAU1452 CLOCK SELECT

PINS	FUNCTION
1-2	OSCILLATOR (MAIN)
2-3	MINI-BOARD CLOCK (LPS)

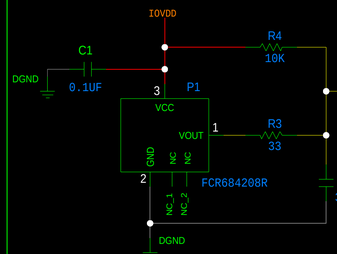


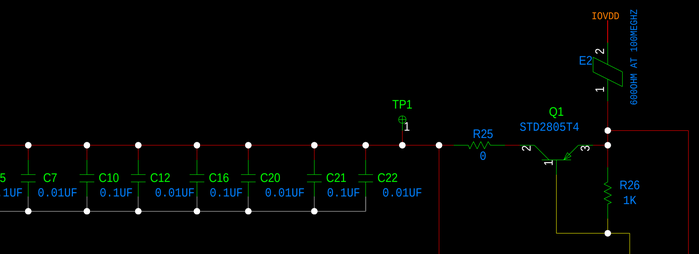
SELF-BOOT DISABLE

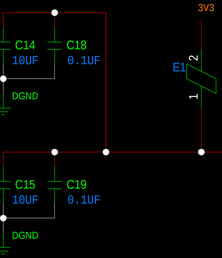
DEFAULT: JUMP 1-2 (DISABLED)



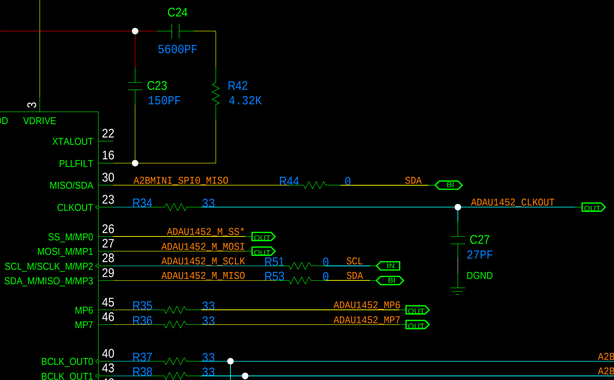
S/PDIF INPUT

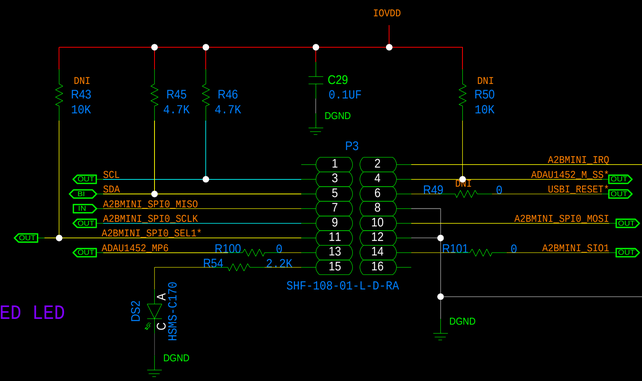




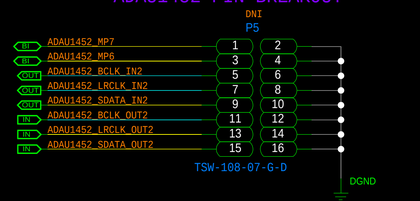




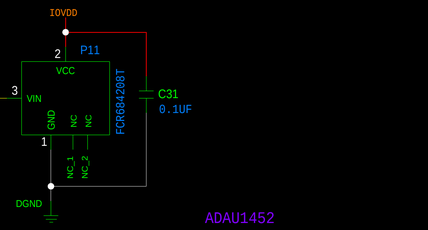


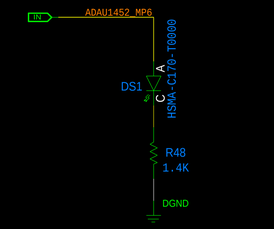


ADAU1452 PIN BREAKOUT

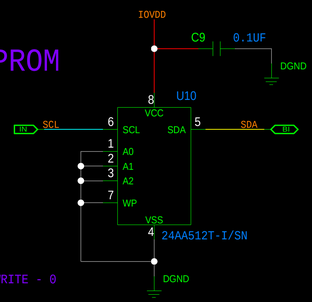


S/PDIF OUTPUT



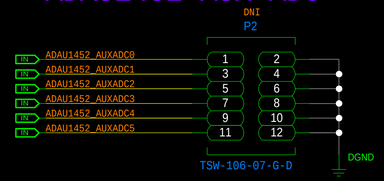


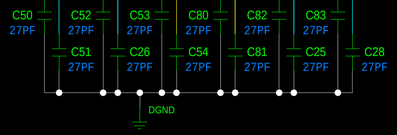
I2C BOOT EEPROM



TWI ADDRESS 1010000X, 0X50
X FOR BIT 0 IS THE R/W BIT. READ - 1, WRITE - 0

ADAU1452 AUX ADC



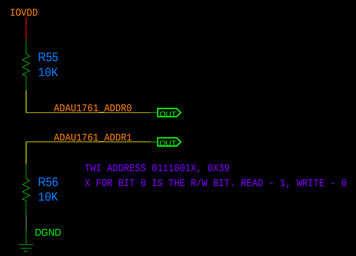


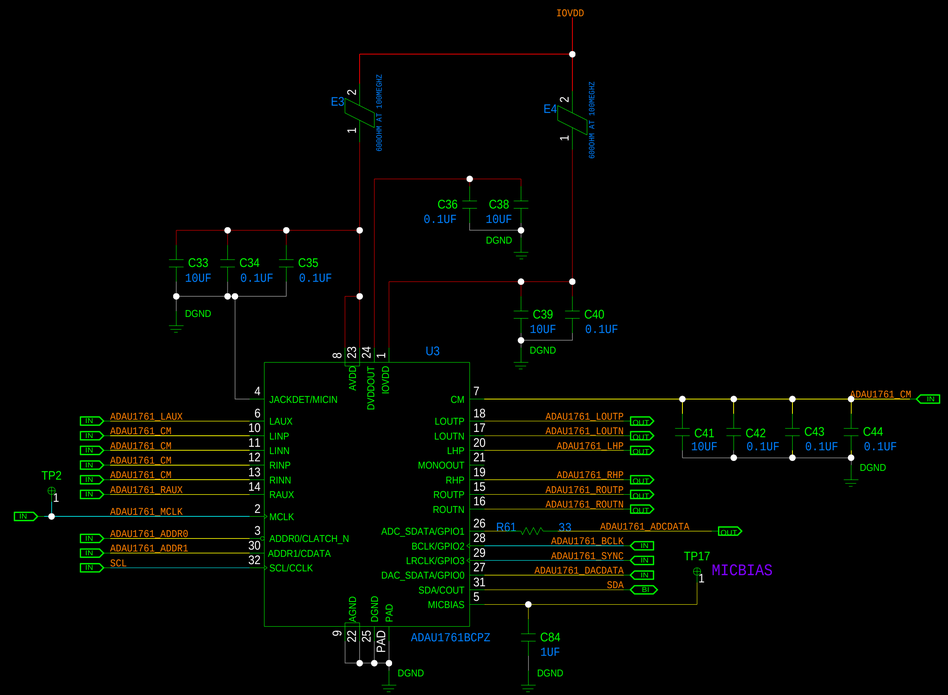
SCHEMATIC	
HW TYPE : Customer Evaluation Z	
Product(s): ADAA245X	
: N/A	
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073460 — REV C
PTD ENGINEER R. WHITNEY	SIZE D — SCALE 1:1 — SHEET 3 OF 6

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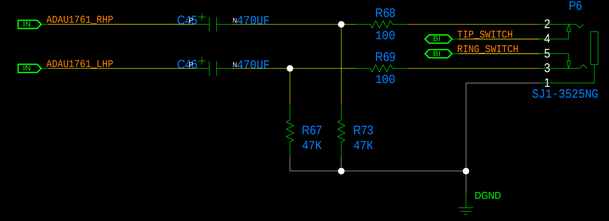
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

I2C ADDRESS

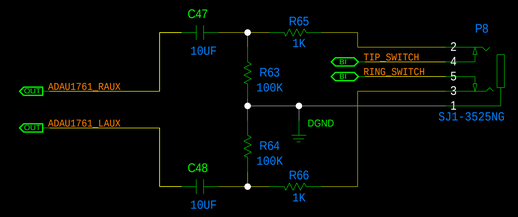




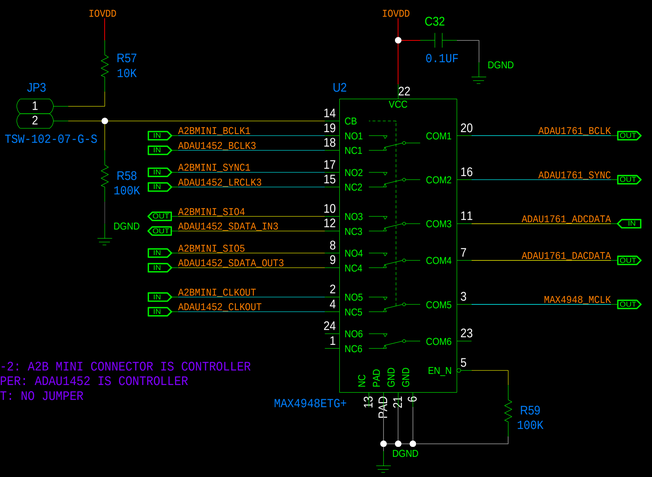
DAC OUTPUT 1



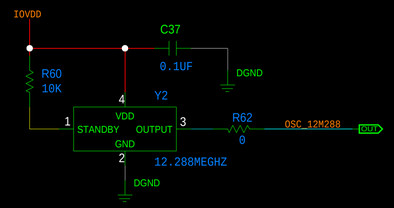
ADC INPUT 1



ADAU1761 I2S/TDM CONTROLLER SELECT

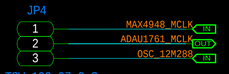


PINS 1-2: A2B MINI CONNECTOR IS CONTROLLER
NO JUMPER: ADAU1452 IS CONTROLLER
DEFAULT: NO JUMPER



ADAU1761 MCLK SOURCE

PINS	FUNCTION
1-2	MAX4948 (DEFAULT)
2-3	OSCILLATOR



MCLK SOURCE FOR ADAU1761 IS CHOSEN FROM EITHER THE OSCILLATOR OR THE SELECTION FROM THE MAX4948
MAX4948 CLK SRC IS EITHER FROM THE ADAU1452 CLKOUT OR CLKOUT FROM A2B MINI CONNECTOR
DEPENDING ON JP3

ANALOG DEVICES

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SCHEMATIC

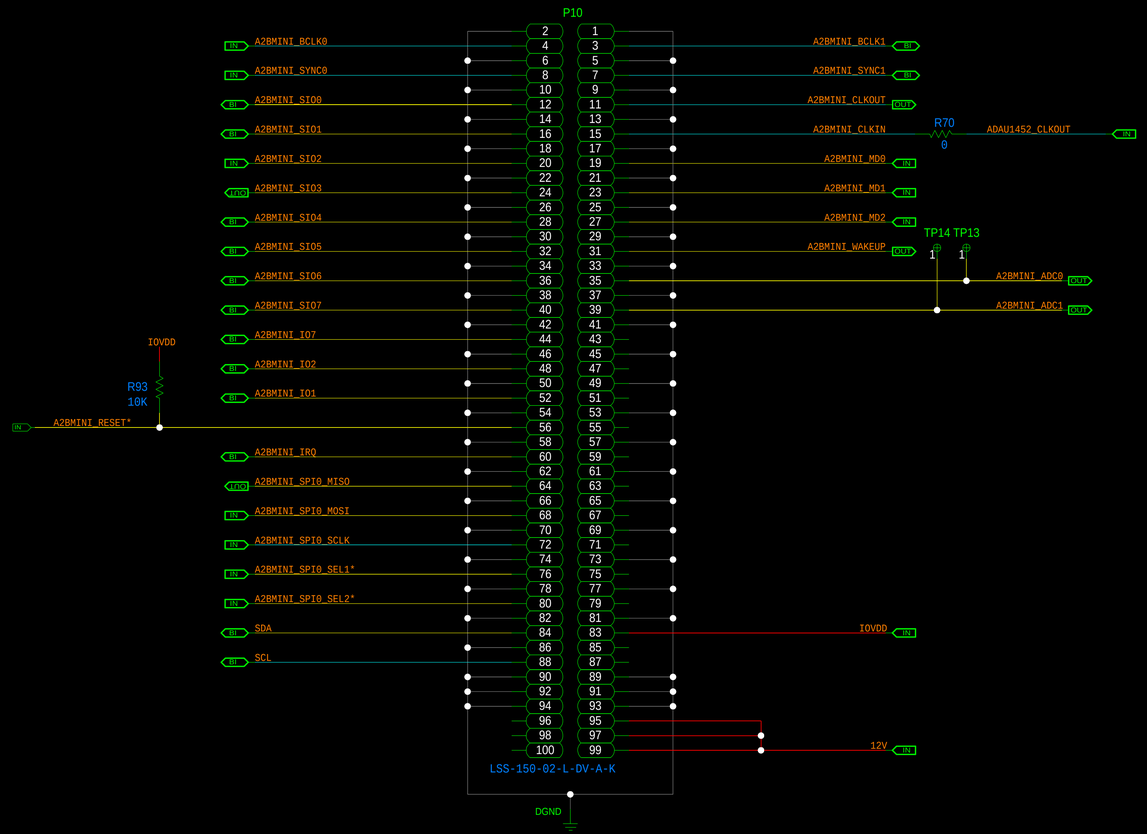
HW TYPE : Customer Evaluation Z
Product(s): ADAA245X
: N/A

DESIGN VIEW	DRAWING NO.	REV
<DESIGN_VIEW>	02_073460	C

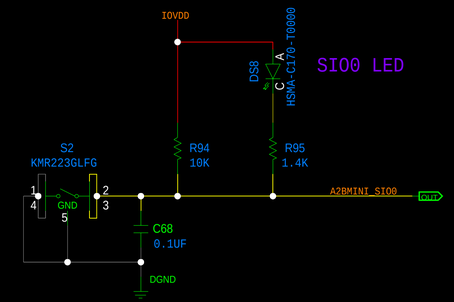
PTD ENGINEER	SIZE	SCALE	SHEET
R. WHITNEY	D	1:1	4 OF 6

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

A2B MINI CONNECTOR



SIO0 PUSHBUTTON





ANALOG DEVICES

SCHEMATIC

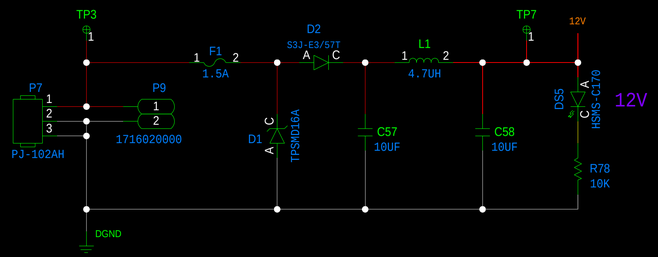
HW TYPE : Customer Evaluation Z
Product(s): ADAA245X
: N/A

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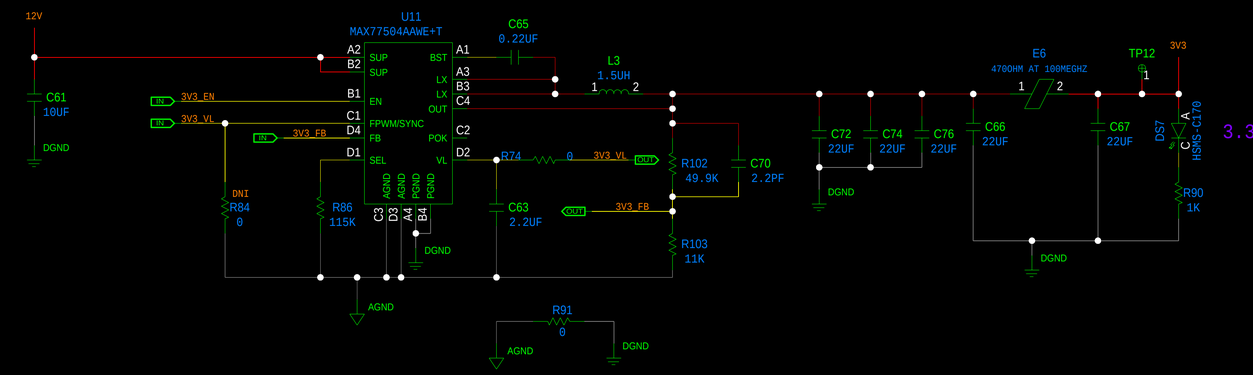
DESIGN VIEW	DRAWING NO.	REV
<DESIGN_VIEW>	02_073460	C
PTD ENGINEER	SIZE / SCALE	SHEET 5 OF 6
R. WHITNEY	D / 1:1	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

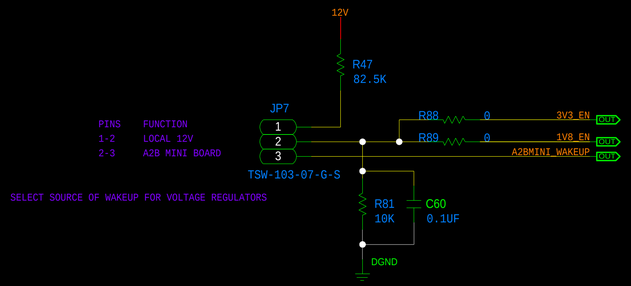
12V BARREL-JACK INPUT



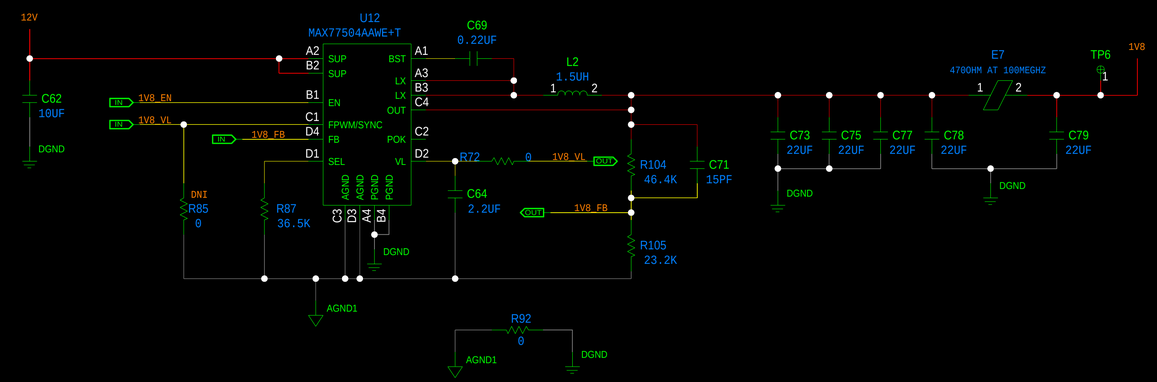
3.3V, 3A



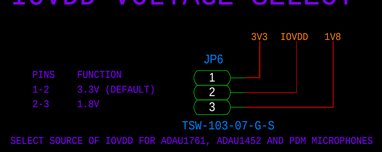
WAKE-UP SOURCE SELECT



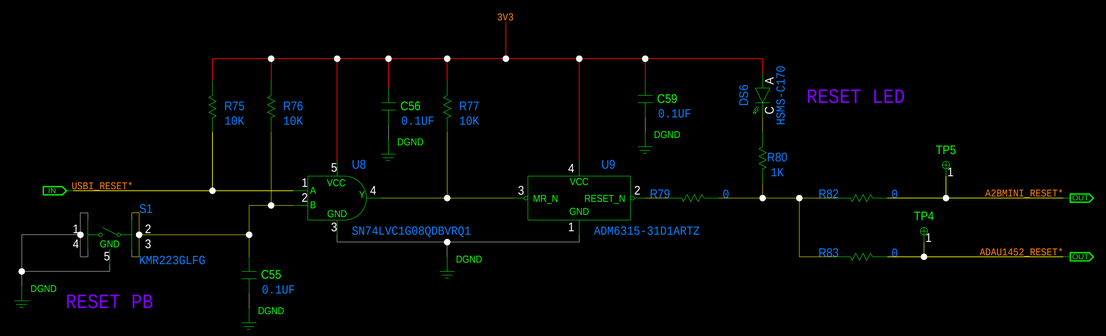
1.8V, 3A



IOVDD VOLTAGE SELECT



RESET



ANALOG DEVICES	SCHEMATIC
	HW TYPE : Customer Evaluation Z Product(s): ADAA245X : N/A
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_073460 / REV C
PTD ENGINEER R. WHITNEY	SIZE D / SCALE 1:1 / SHEET 6 OF 6

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