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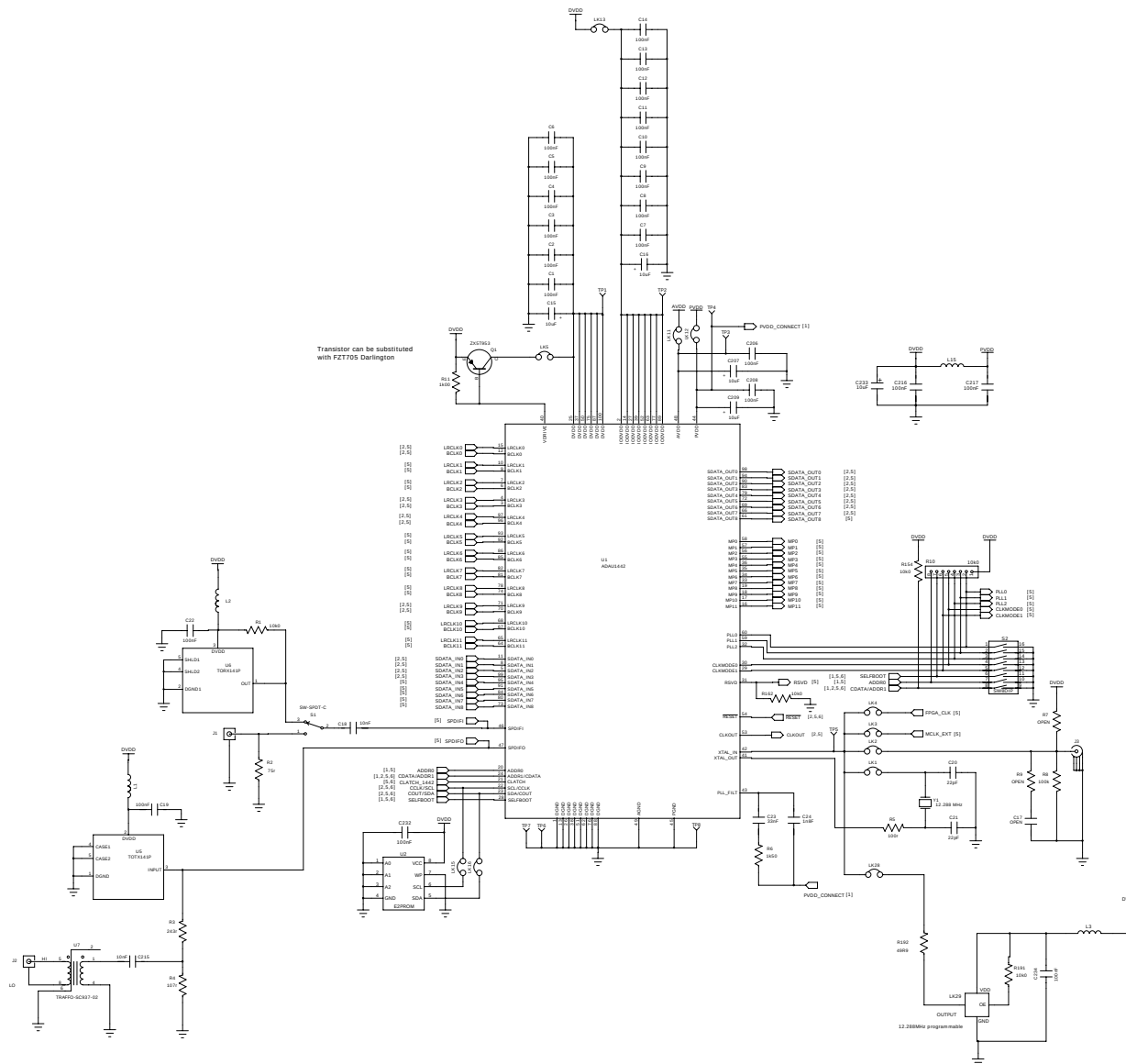
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2

1

REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE



DESIGNED BY	Jerad Lewis	DATE	Sep 2006
CHECKED BY	Coleman Rogers	DATE	070806
QUALITY CONTROL		DATE	
RELEASED		DATE	

COMPANY			
ANALOG DEVICES			
TITLE			
ADAU1442 EVALUATION BOARD			
ADAU1442 SigmaDSP			
CODE	SIZE	DRAWING NO.	REV.
D	EVAL-ADAU1442EB	A	A
SCALE	SHEET 3 of 6		

6

5

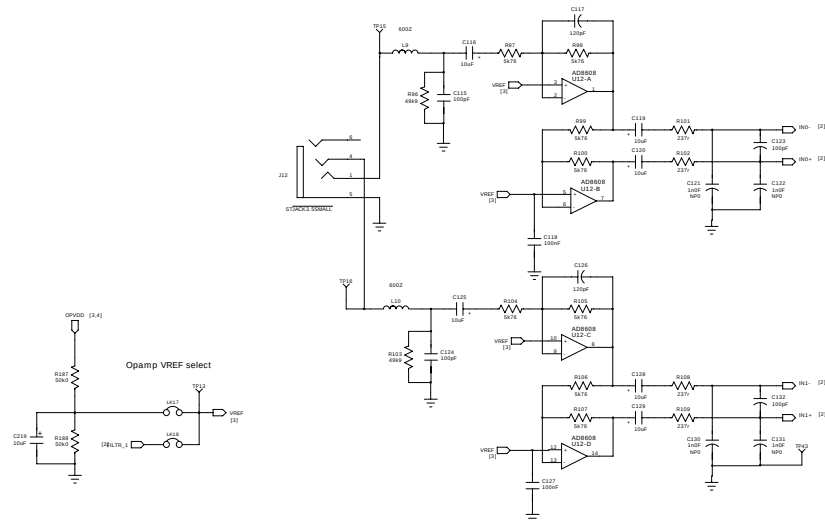
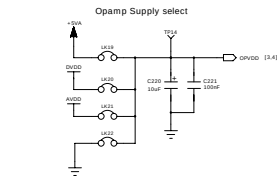
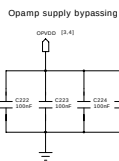
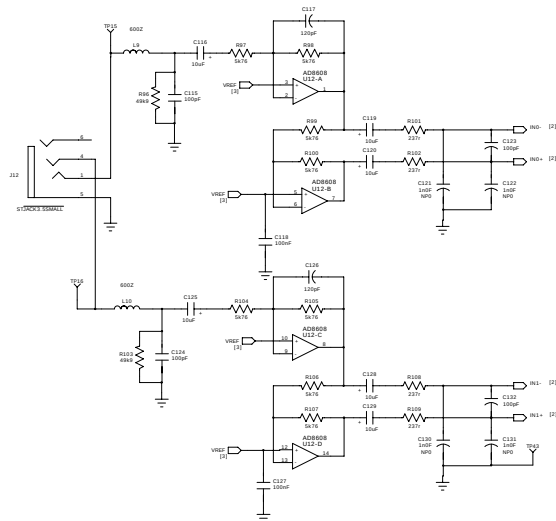
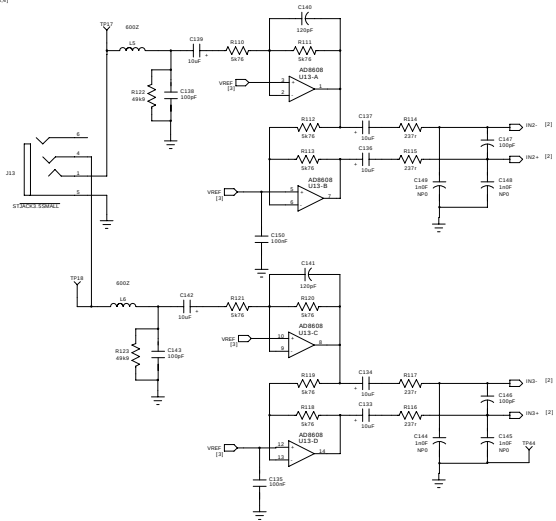
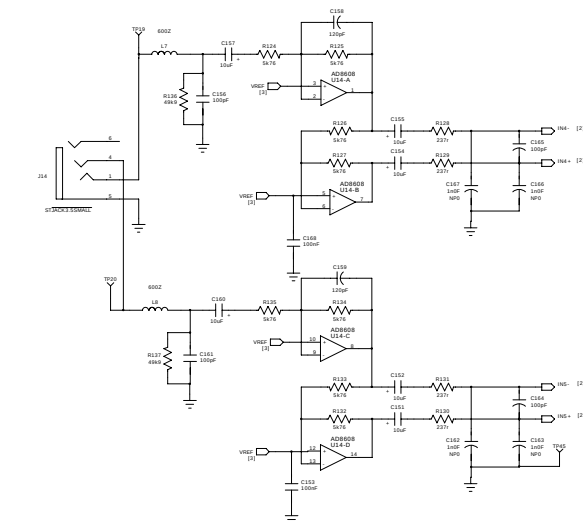
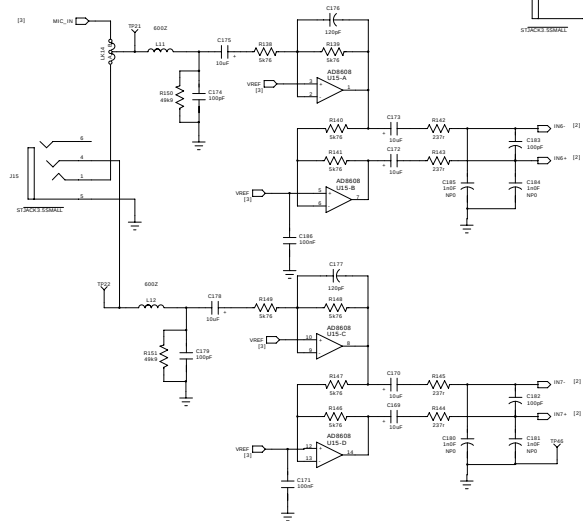
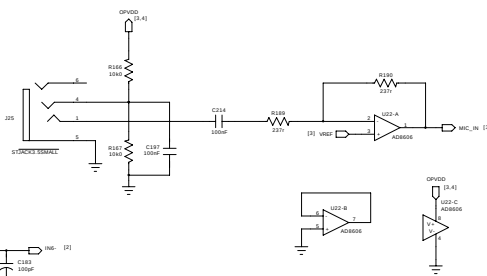
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REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE



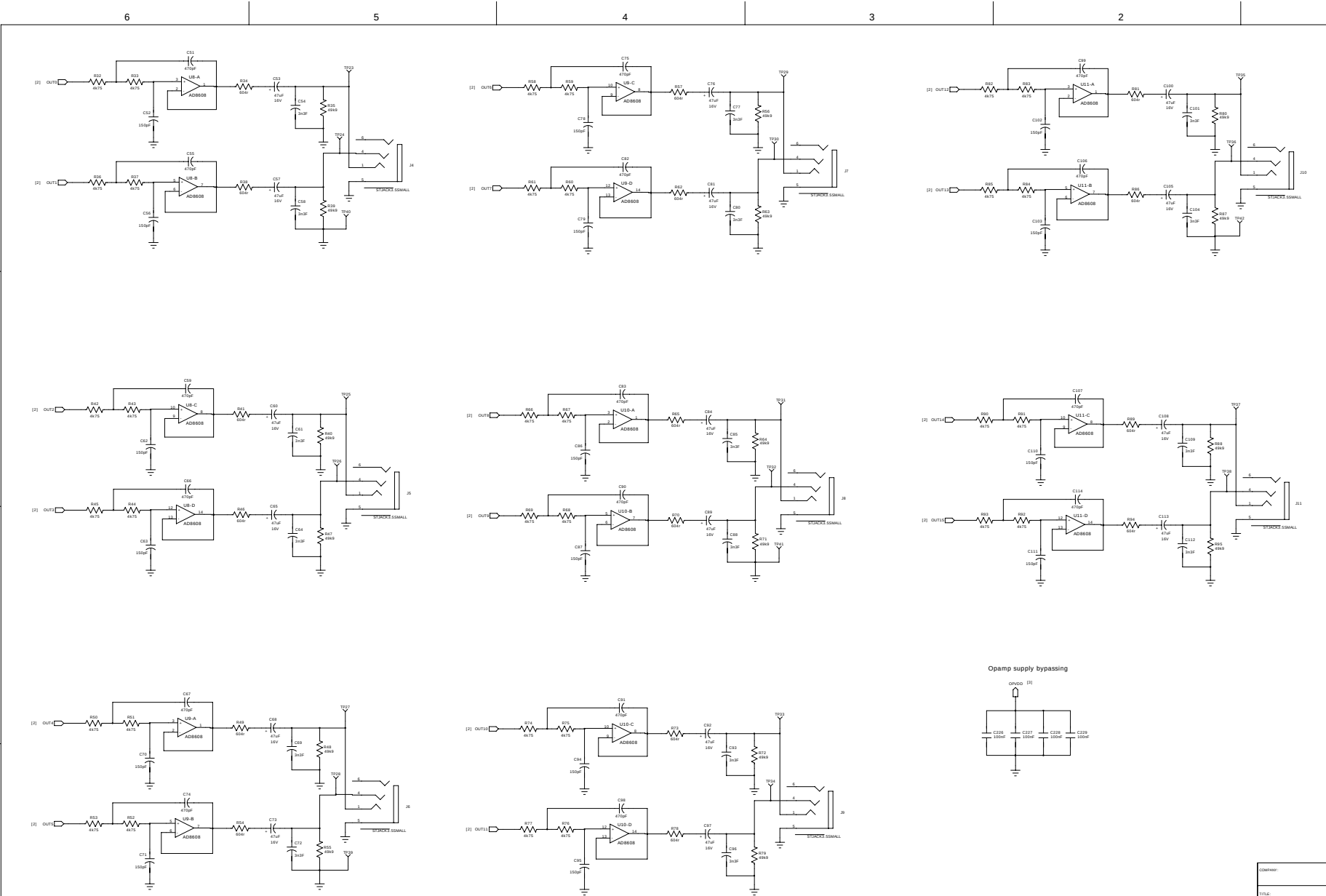
COMPANY			
ANALOG DEVICES			
TITLE			
ADAU1442 EVALUATION BOARD			
Analog Input Filters			
DESIGN	DATE	CODE	SIZE
Jerad Lewis	Sep 2006		
CHECKED	DATE		
Coleman Rogers	070806		
QUALITY CONTROL	DATE		
RELEASED	DATE		
DRAWING NO.		REV.	
D EVAL-ADAU1442EB		A	
SCALE		SHEET 6	

D

C

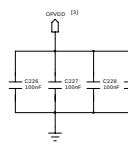
B

A



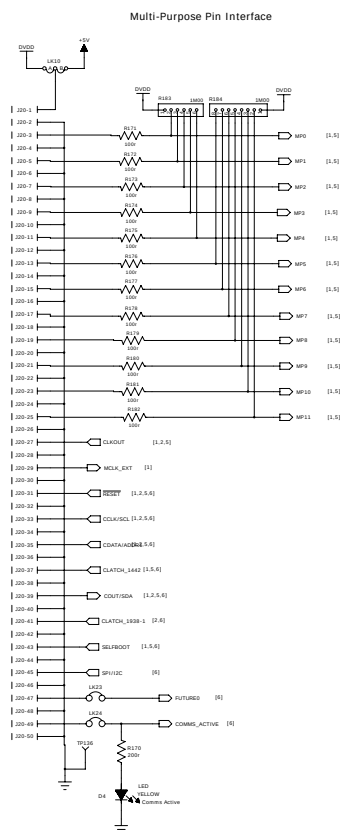
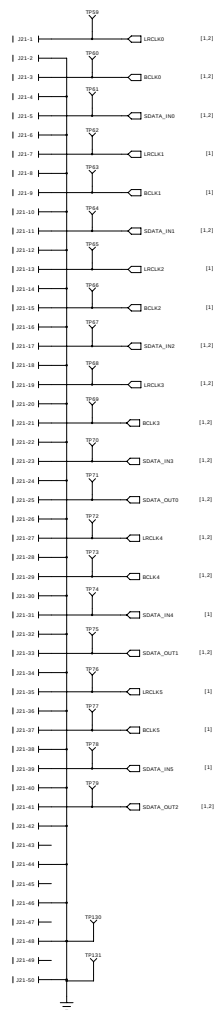
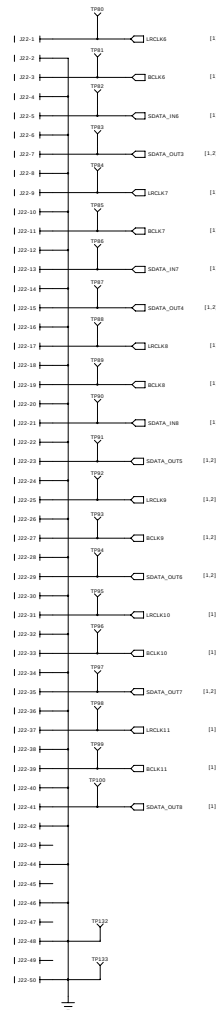
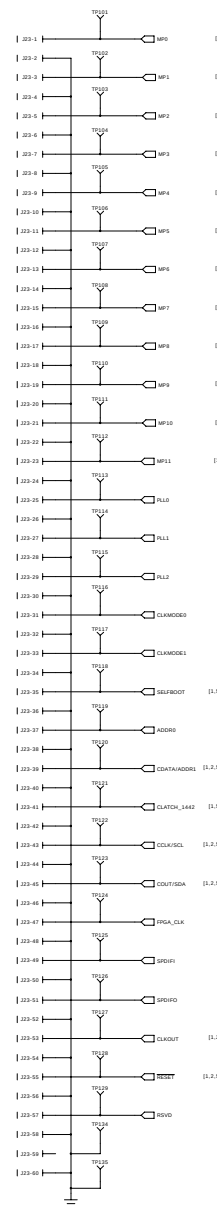
REVISION RECORD			
LTN	ECO NO.	APPROVED	DATE

Opamp supply bypassing



COMPANY			
ANALOG DEVICES			
TITLE			
ADAU1442 EVALUATION BOARD			
Analog Output Filters			
DESIGN	Jerad Lewis	DATE	Sep 2006
CHECKED	Coleman Rogers	DATE	070806
QUALITY CONTROL			
RELEASED			
CODE	D	SIC	
DRAWING NO.	EVAL-ADAU1442EB	REV.	A
SCALE		SHEET	6

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: Jerad Lewis		DATE: Sep 2006		ADAU1442 EVALUATION BOARD Digital Connectors			
CHECKED: Coleman Rogers		DATE: 070806					
QUALITY CONTROL:		DATE:		CODE:	SIZE: D	DRAWING NO: EVAL-ADAU1442EB	REV: A
RELEASED:		DATE:		SCALE:		SHEET: 5	6

