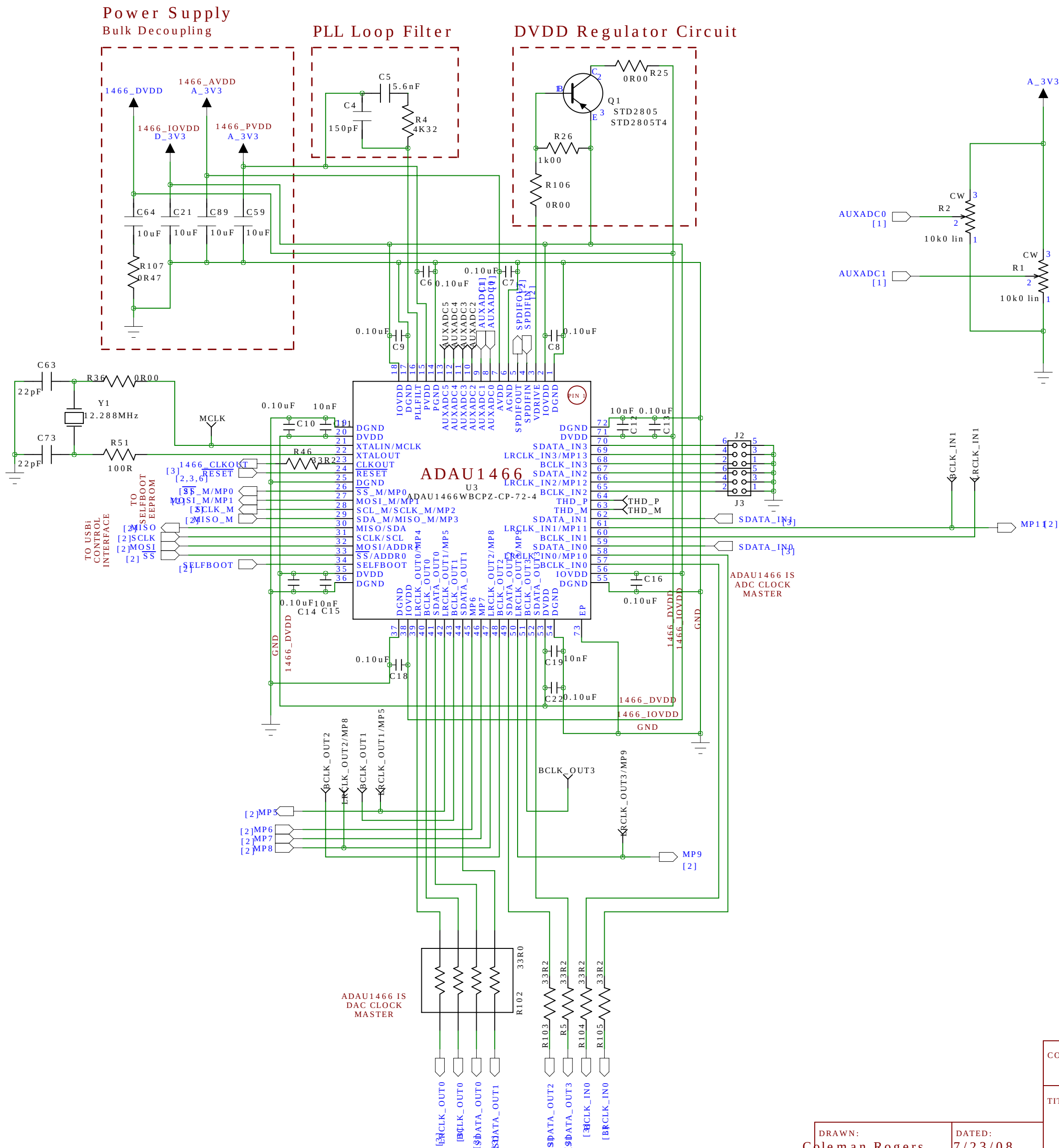


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

SigmaDSP Audio Processor



COMPANY:			
TITLE: EVAL-ADAU1466Z ADAU1466			
DRAWN: Coleman Rogers	DATED: 7/23/08	CODE:	SIZE:
CHECKED: Brett Gildersleeve	DATED: <Checked Date>	DRAWING NO:	REV:
QUALITY CONTROL: Khiem Nguyen	DATED: <QC Date>	< Code >	C
RELEASED: <Released By>	DATED: <Release Date>	DATE: < Scale >	13-Feb-18
SHEET: 1 OF 6			3

6

5

4

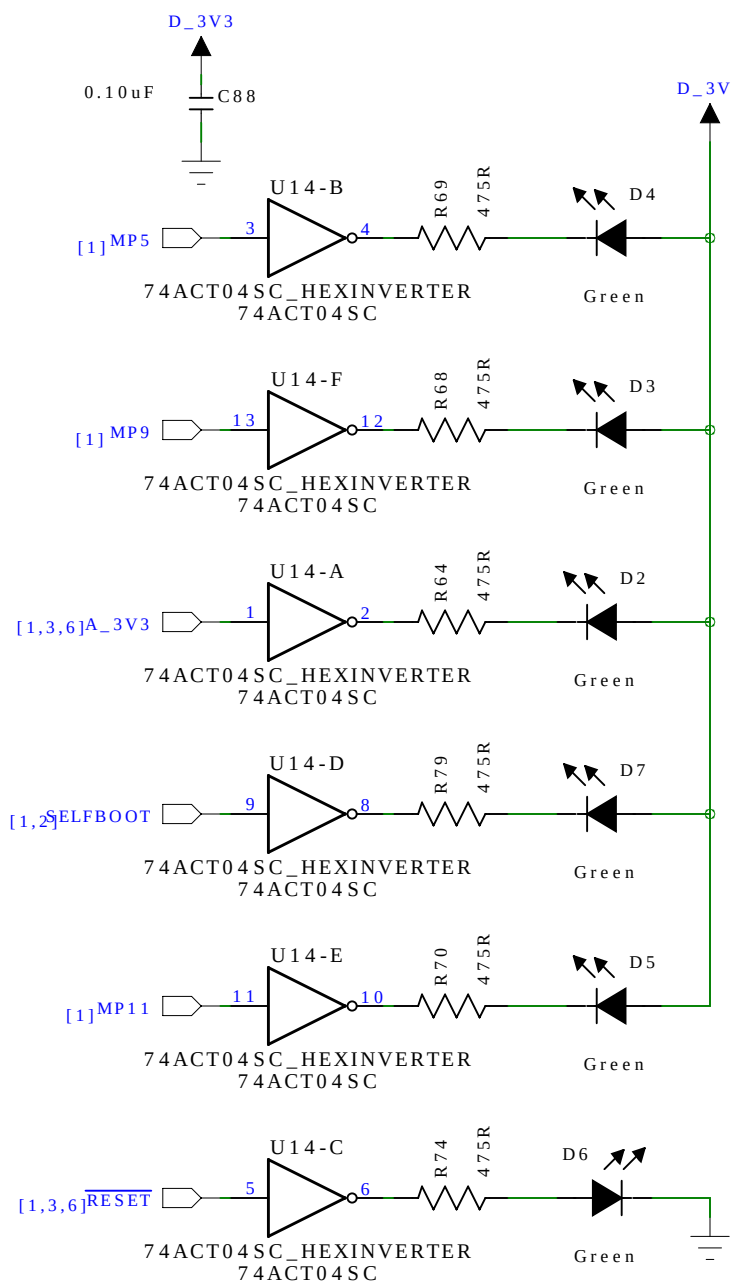
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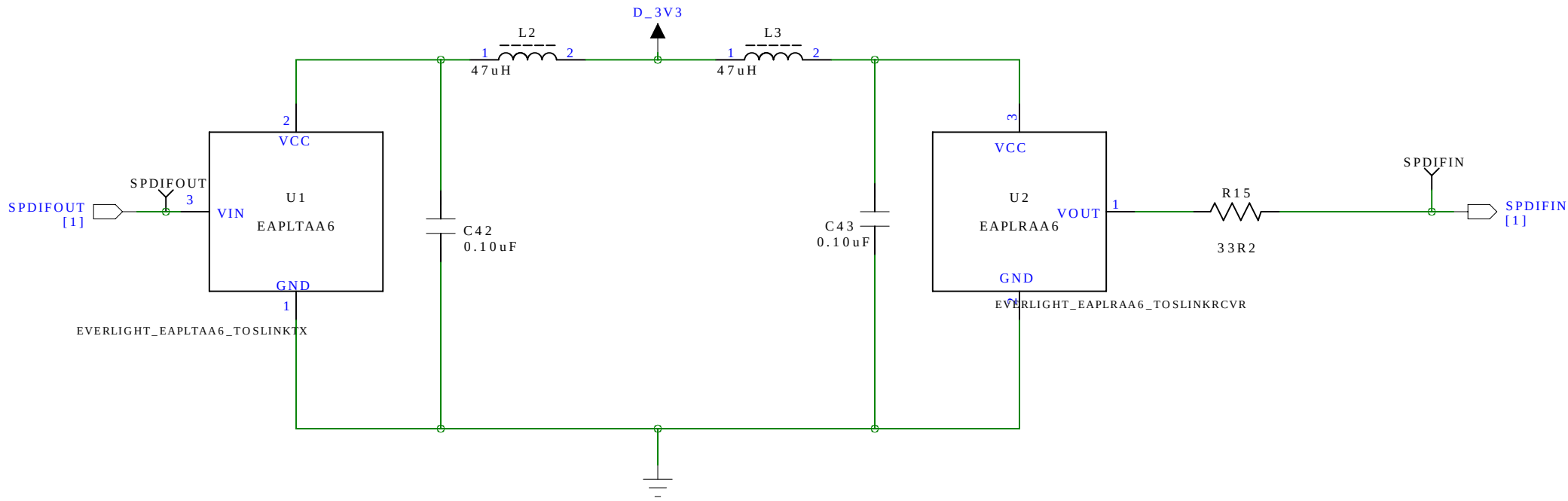
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

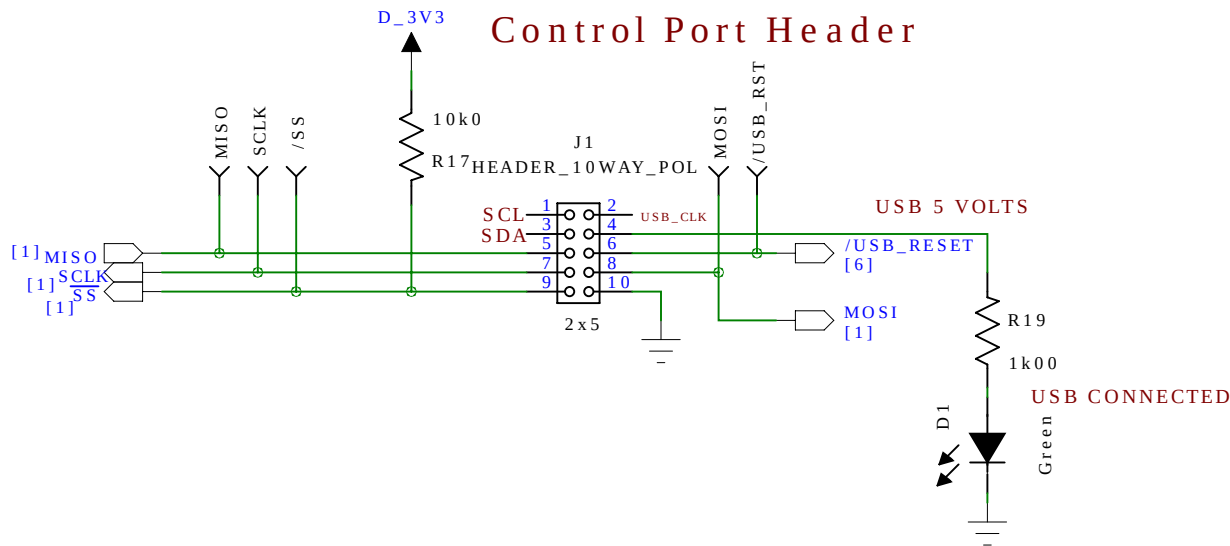
Status LEDs



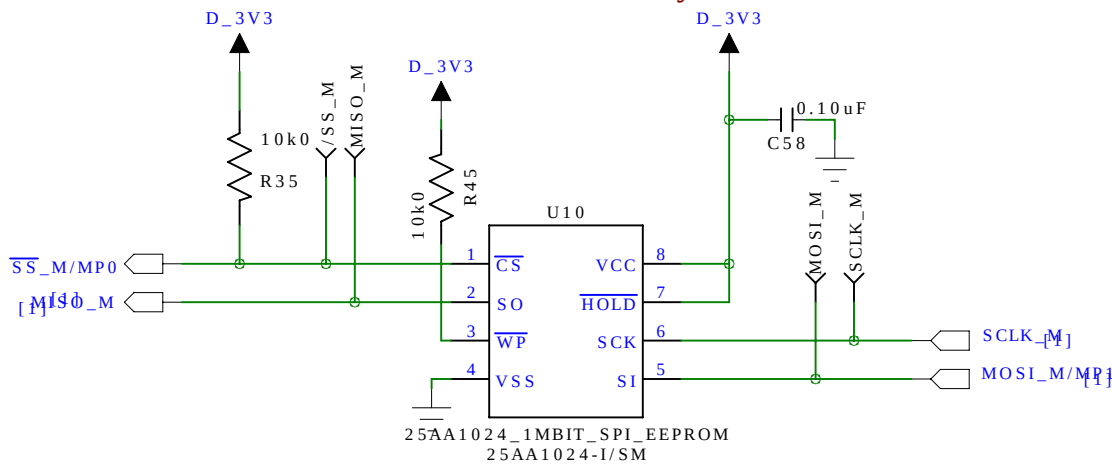
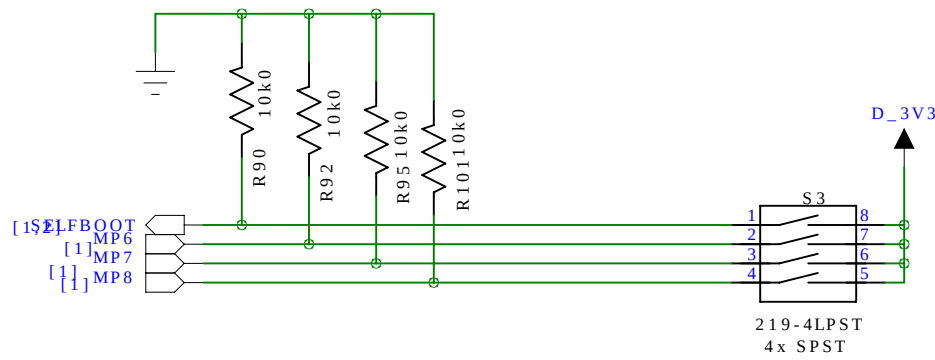
S/PDIF Optical Connectors



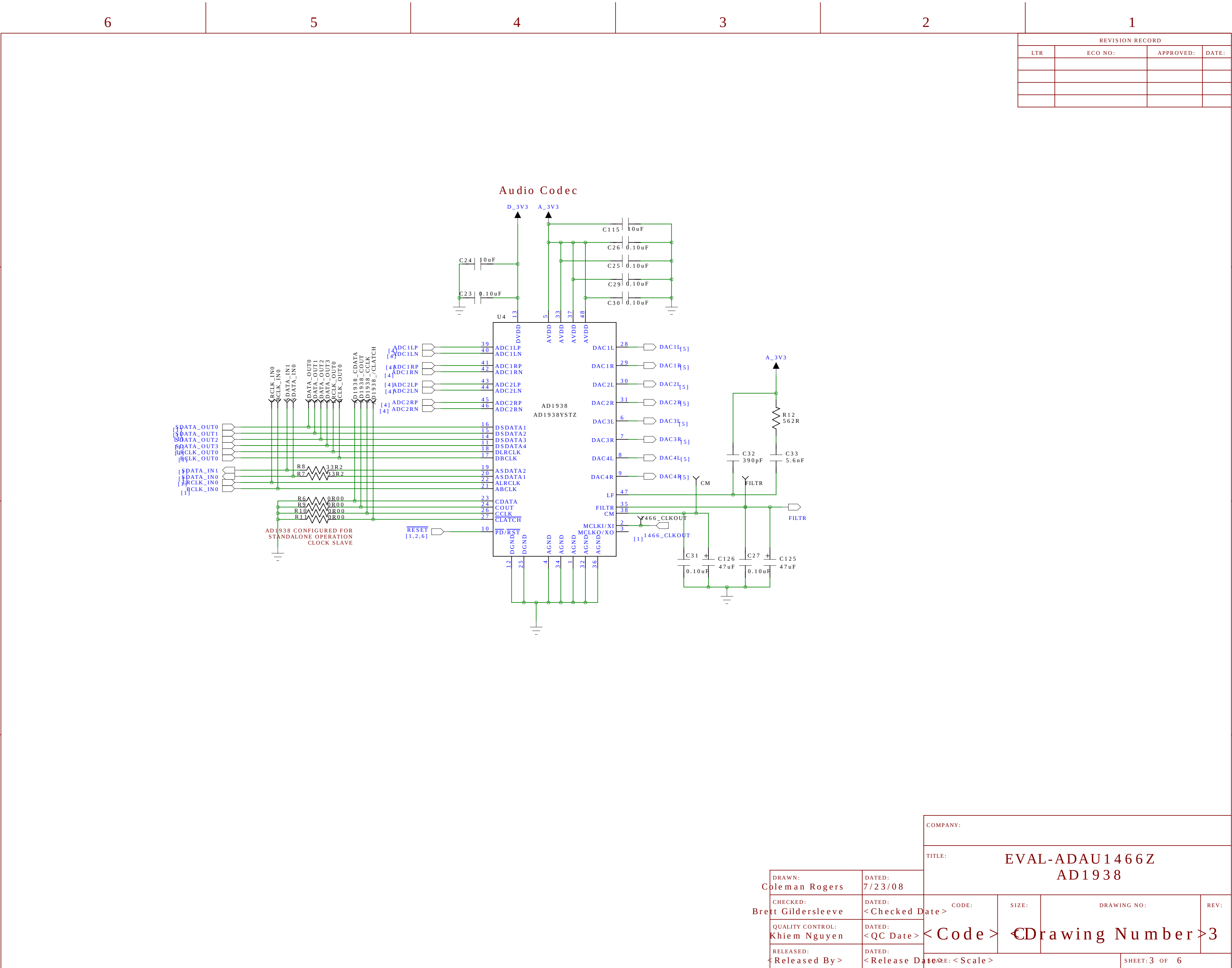
Control Port Header



Self-Boot Memory



COMPANY:			
TITLE:			
EVAL-ADAU1466Z ADAU1466_PERIPHERALS			
DRAWN: Coleman Rogers	DATED: 7/23/08	CODE:	SIZE:
CHECKED: Brett Gildersleeve	DATED: <Checked Date>	DRAWING NO:	REV:
QUALITY CONTROL: Khiem Nguyen	DATED: <QC Date>	< Code > < Drawing Number > 3	
RELEASED: <Released By>	DATED: <Release Date>	SHEET: 2 OF 6	



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY:			
TITLE: EVAL-ADAU1466Z AD1938			
CODE:	SIZE:	DRAWING NO:	REV:
< Code >	< Drawing Number >	3	
DATE: < Scale >			SHEET: 3 OF 6

DRAWN: Coleman Rogers	DATED: 7/23/08
CHECKED: Brett Gildersleeve	DATED: < Checked Date >
QUALITY CONTROL: Khiem Nguyen	DATED: < QC Date >
RELEASED: < Released By >	DATED: < Release Date >

