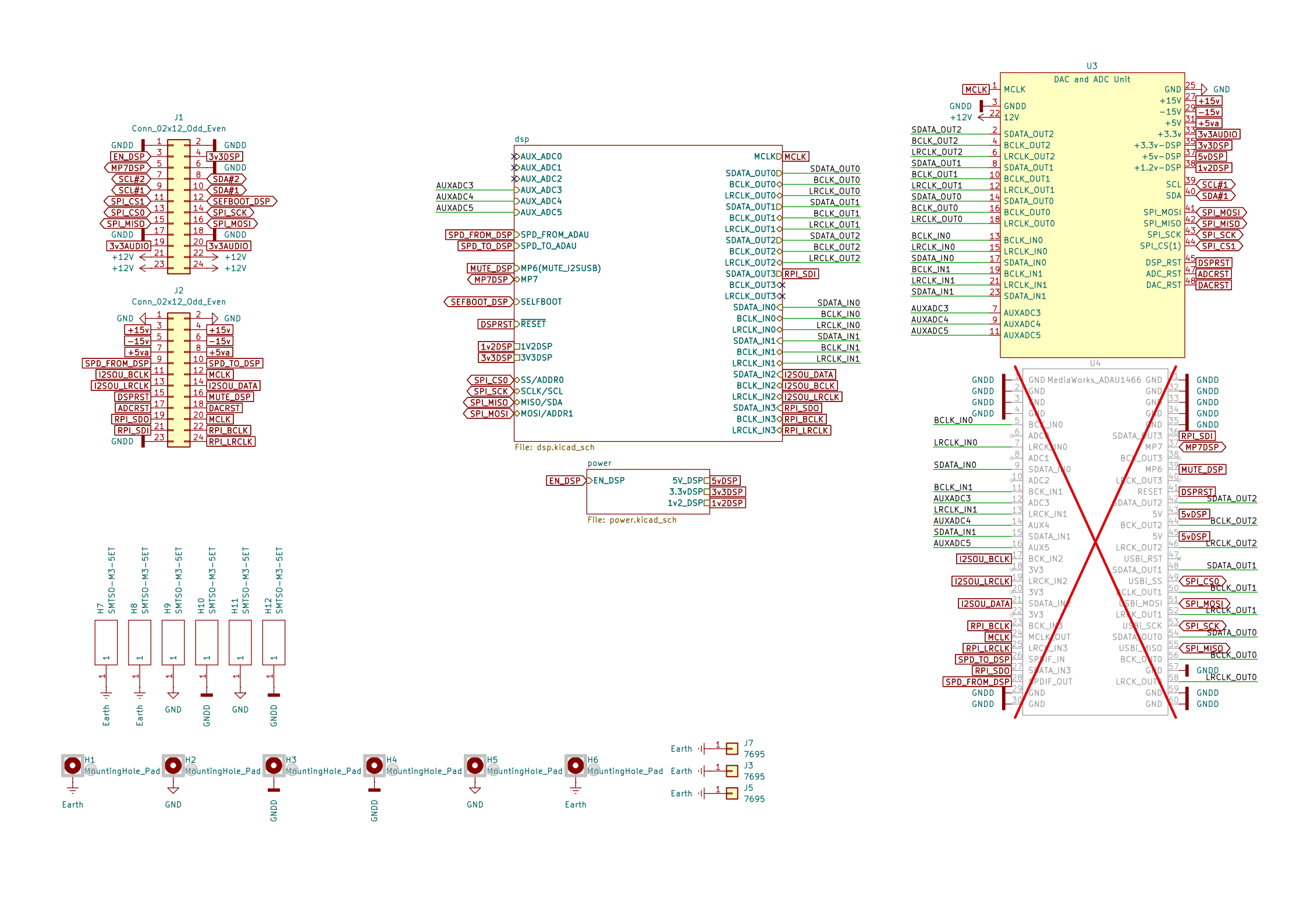
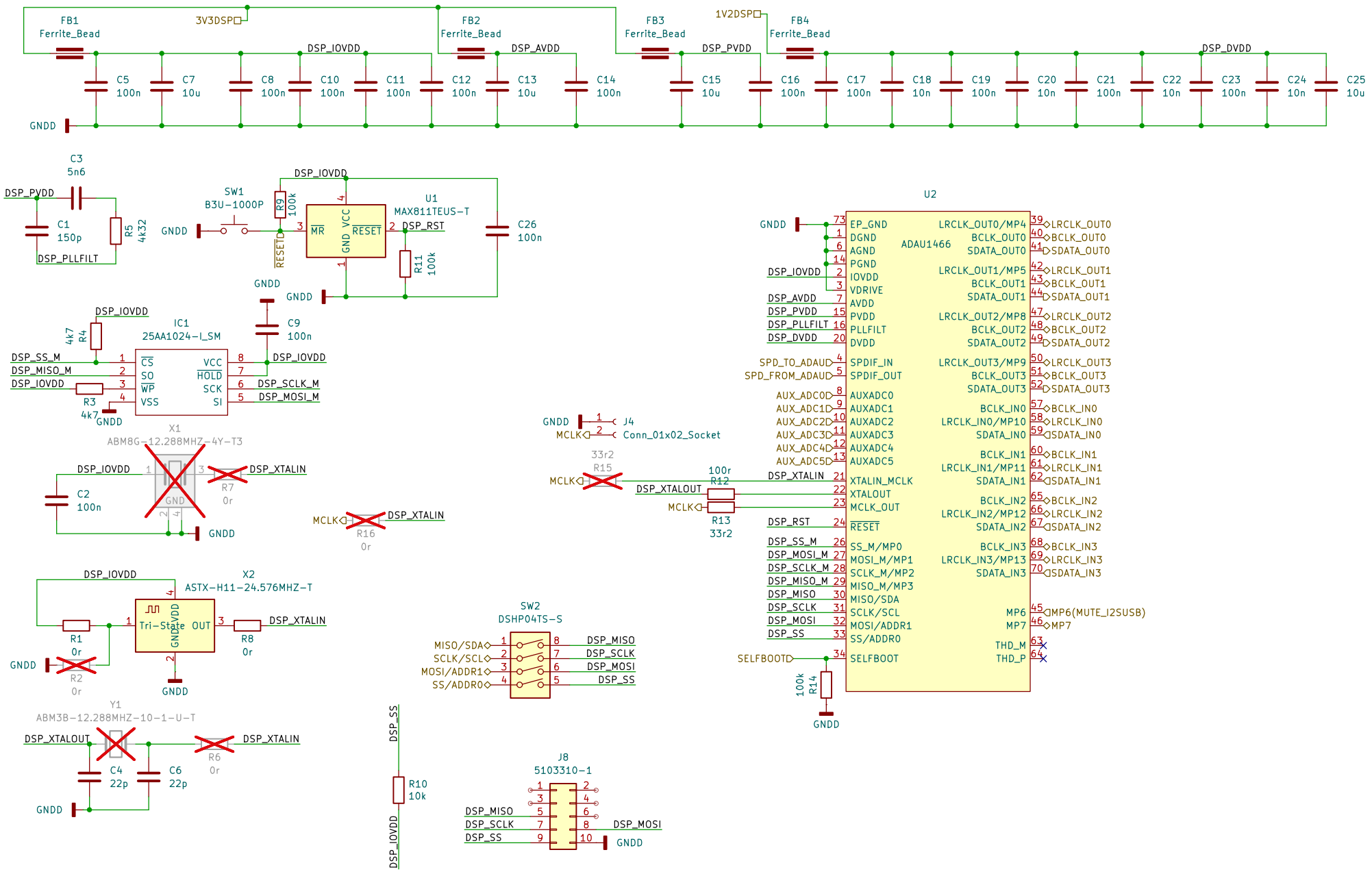
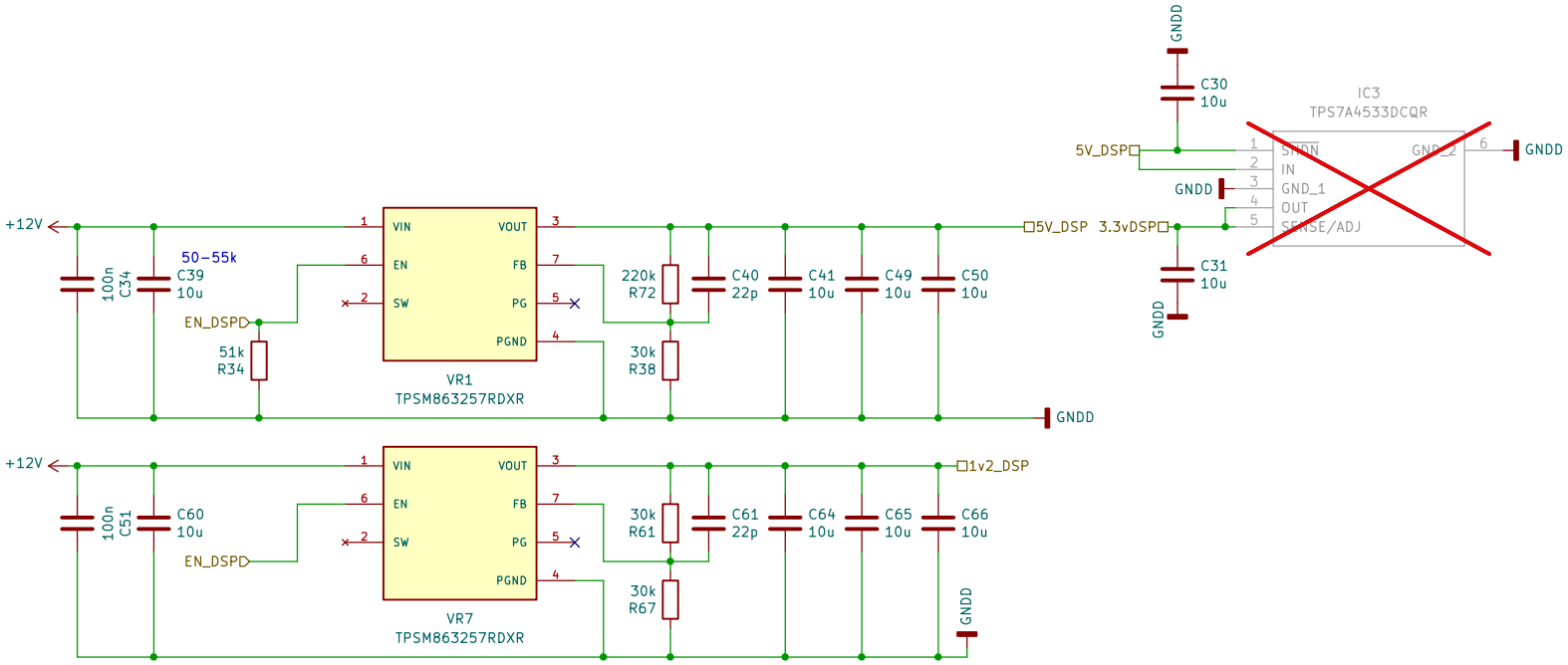

J1
Conn_02x12_Odd_Even
GNDD
EN_DSP
MP7DSP
SCL#2
SCL#1
SPI_CS1
SPI_CS0
SPI_MISO
3v3AUDIO
3v3DSP
SDA#2
SDA#1
SEFBOOT_DSP
SPI_SCK
SPI_MOSI
+12V
J2
Conn_02x12_Odd_Even
GND
+15v
-15v
+5va
SPD_FROM_DSP
I2SOU_BCLK
I2SOU_LRCLK
DSPRST
ADCRST
RPI_SDO
RPI_SDI
SPD_TO_DSP
MCLK
I2SOU_DATA
MUTE_DSP
DACRST
RPI_BCLK
RPI_LRCLK
dsp
AUX_ADC0
AUX_ADC1
AUX_ADC2
AUX_ADC3
AUX_ADC4
AUX_ADC5
AUXADC3
AUXADC4
AUXADC5
SPD_FROM_ADAU
SPD_TO_ADAU
MP6(MUTE_I2SUSB)
MP7
SELFBOOT
RESET
1V2DSP
3V3DSP
SS/ADDR0
SCLK/SCL
MISO/SDA
MOSI/ADDR1
SDATA_OUT0
BCLK_OUT0
LRCLK_OUT0
SDATA_OUT1
BCLK_OUT1
LRCLK_OUT1
SDATA_OUT2
BCLK_OUT2
LRCLK_OUT2
SDATA_OUT3
BCLK_OUT3
LRCLK_OUT3
SDATA_IN0
BCLK_IN0
LRCLK_IN0
SDATA_IN1
BCLK_IN1
LRCLK_IN1
SDATA_IN2
BCLK_IN2
LRCLK_IN2
SDATA_IN3
BCLK_IN3
LRCLK_IN3
File: dsp.kicad_sch
power
EN_DSP
5V_DSP
3.3vDSP
1v2_DSP
File: power.kicad_sch
U3
DAC and ADC Unit
MCLK
GNDD
+12V
12V
SDATA_OUT2
BCLK_OUT2
LRCLK_OUT2
SDATA_OUT1
BCLK_OUT1
LRCLK_OUT1
SDATA_OUT0
BCLK_OUT0
LRCLK_OUT0
BCLK_IN0
LRCLK_IN0
SDATA_IN0
BCLK_IN1
LRCLK_IN1
SDATA_IN1
AUXADC3
AUXADC4
AUXADC5
GND
+15v
-15v
+5va
+3.3v
+3.3v-DSP
+5v-DSP
+1.2v-DSP
SCL
SDA
SPI_MOSI
SPI_MISO
SPI_SCK
SPI_CS(1)
DSP_RST
ADC_RST
DAC_RST
SCL#1
SDA#1
3v3AUDIO
3v3DSP
5vDSP
1v2DSP
DSPRST
ADCRST
DACRST
U4
GND MediaWorks_ADAU1466
ADC0
ADC1
ADC2
ADC3
AUX4
AUX5
3V3
BCK_IN2
LRCK_IN2
SDATA_IN2
SDATA_IN3
MCLK_OUT
SPDIF_IN
SPDIF_OUT
BCK_IN0
LRCK_IN0
SDATA_IN0
BCK_IN1
LRCK_IN1
SDATA_IN1
BCK_OUT3
MP7
MP6
LRCK_OUT3
RESET
SDATA_OUT2
5V
BCK_OUT2
LRCK_OUT2
USBi_RST
SDATA_OUT1
USBi_SS
BCK_OUT1
USBi_MOSI
LRCK_OUT1
USBi_SCK
SDATA_OUT0
USBi_MISO
BCK_OUT0
LRCK_OUT0
H7 SMTSO-M3-5ET
H8 SMTSO-M3-5ET
H9 SMTSO-M3-5ET
H10 SMTSO-M3-5ET
H11 SMTSO-M3-5ET
H12 SMTSO-M3-5ET
Earth
GND
GNDD
H1 MountingHole_Pad
H2 MountingHole_Pad
H3 MountingHole_Pad
H4 MountingHole_Pad
H5 MountingHole_Pad
H6 MountingHole_Pad
J7 7695
J3 7695
J5 7695


FB1
Ferrite_Bead
3V3DSP
FB2
Ferrite_Bead
FB3
Ferrite_Bead
1V2DSP
FB4
Ferrite_Bead
DSP_IOVDD
DSP_AVDD
DSP_PVDD
DSP_DVDD
GNDD
C5 100n
C7 10u
C8 100n
C10 100n
C11 100n
C12 100n
C13 10u
C14 100n
C15 10u
C16 100n
C17 100n
C18 10n
C19 100n
C20 10n
C21 100n
C22 10n
C23 100n
C24 10n
C25 10u
C3 5n6
C1 150p
R5 4k32
DSP_PLLFILT
SW1
B3U-1000P
R9 100k
RESET
U1
MAX811TEUS-T
MR
GND
VCC
RESET
DSP_RST
R11 100k
C26 100n
C9 100n
IC1
25AA1024-I_SM
R4 4k7
R3 4k7
DSP_SS_M
DSP_MISO_M
DSP_IOVDD
CS
SO
WP
VSS
VCC
HOLD
SCK
SI
DSP_SCLK_M
DSP_MOSI_M
X1
ABM8G-12.288MHZ-4Y-T3
C2 100n
R7 0r
DSP_XTALIN
GND
MCLK
R16 0r
J4
Conn_01x02_Socket
R15 33r2
R12 100r
R13 33r2
DSP_XTALOUT
X2
ASTX-H11-24.576MHZ-T
Tri-State
OUT
R1 0r
R2 0r
R8 0r
Y1
ABM3B-12.288MHZ-10-1-U-T
C4 22p
C6 22p
R6 0r
SW2
DSHP04TS-S
MISO/SDA
SCLK/SCL
MOSI/ADDR1
SS/ADDR0
DSP_MISO
DSP_SCLK
DSP_MOSI
DSP_SS
R10 10k
J8
5103310-1
U2
ADAU1466
EP_GND
DGND
AGND
PGND
IOVDD
VDRIVE
AVDD
PVDD
PLLFILT
DVDD
SPDIF_IN
SPDIF_OUT
AUXADC0
AUXADC1
AUXADC2
AUXADC3
AUXADC4
AUXADC5
XTALIN_MCLK
XTALOUT
MCLK_OUT
RESET
SS_M/MP0
MOSI_M/MP1
SCLK_M/MP2
MISO_M/MP3
MISO/SDA
SCLK/SCL
MOSI/ADDR1
SS/ADDR0
SELFBOOT
LRCLK_OUT0/MP4
BCLK_OUT0
SDATA_OUT0
LRCLK_OUT1/MP5
BCLK_OUT1
SDATA_OUT1
LRCLK_OUT2/MP8
BCLK_OUT2
SDATA_OUT2
LRCLK_OUT3/MP9
BCLK_OUT3
SDATA_OUT3
BCLK_IN0
LRCLK_IN0/MP10
SDATA_IN0
BCLK_IN1
LRCLK_IN1/MP11
SDATA_IN1
BCLK_IN2
LRCLK_IN2/MP12
SDATA_IN2
BCLK_IN3
LRCLK_IN3/MP13
SDATA_IN3
MP6
MP7
THD_M
THD_P
R14 100k


+12V
100n
C34
C39
10u
50-55k
EN_DSPD
51k
R34
VIN
EN
SW
VOUT
FB
PG
PGND
VR1
TPSM863257RDXR
220k
R72
30k
R38
C40
22p
C41
10u
C49
10u
C50
10u
5V_DSP
3.3vDSP
GNDD
C30
10u
C31
10u
IC3
TPS7A4533DCQR
SHDN
IN
GND_1
OUT
SENSE/ADJ
GND_2
+12V
100n
C51
C60
10u
EN_DSPD
VR7
TPSM863257RDXR
30k
R61
30k
R67
C61
22p
C64
10u
C65
10u
C66
10u
1v2_DSP
GNDD